

Fig. 2

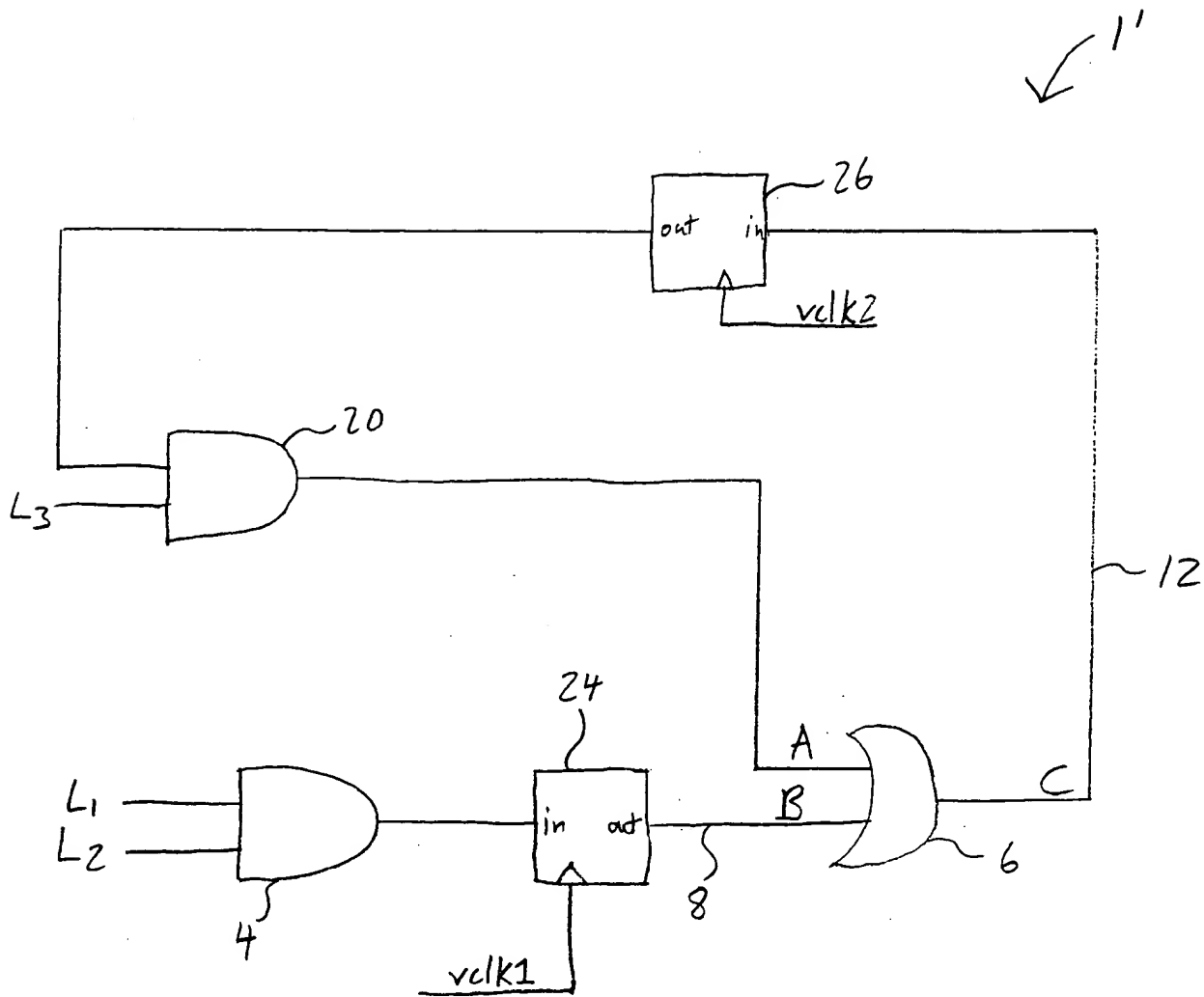


Fig. 3A

~ 27

```

and      g14 ( in24, L1, L2 );
vdelement g24 ( out24, vclk1, in24 );
or       g16 ( in26, A, out24 );
vdelement g26 ( out26, vclk2, in26 );
and      g20 ( A, L3, out26 );

```

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```
table
// vclk  data  out    out_new
  1      1 :   ?      : 1 ;
  1      0 :   ?      : 0 ;
  0      ? :   ?      :- ; // - means 'no change', i.e. retain previous value
endtable
endprimitive
```

Fig. 3B

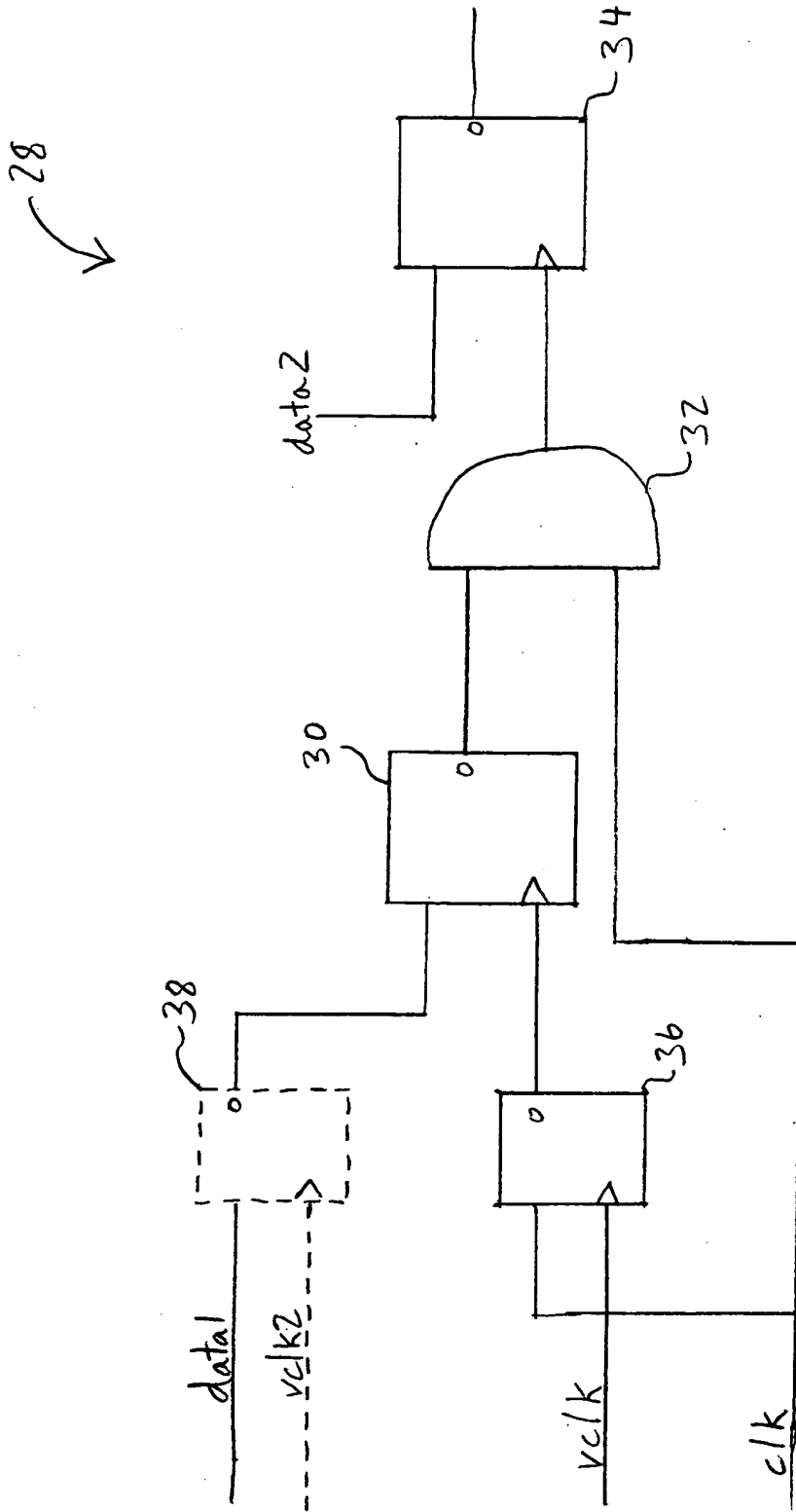


Fig. 4

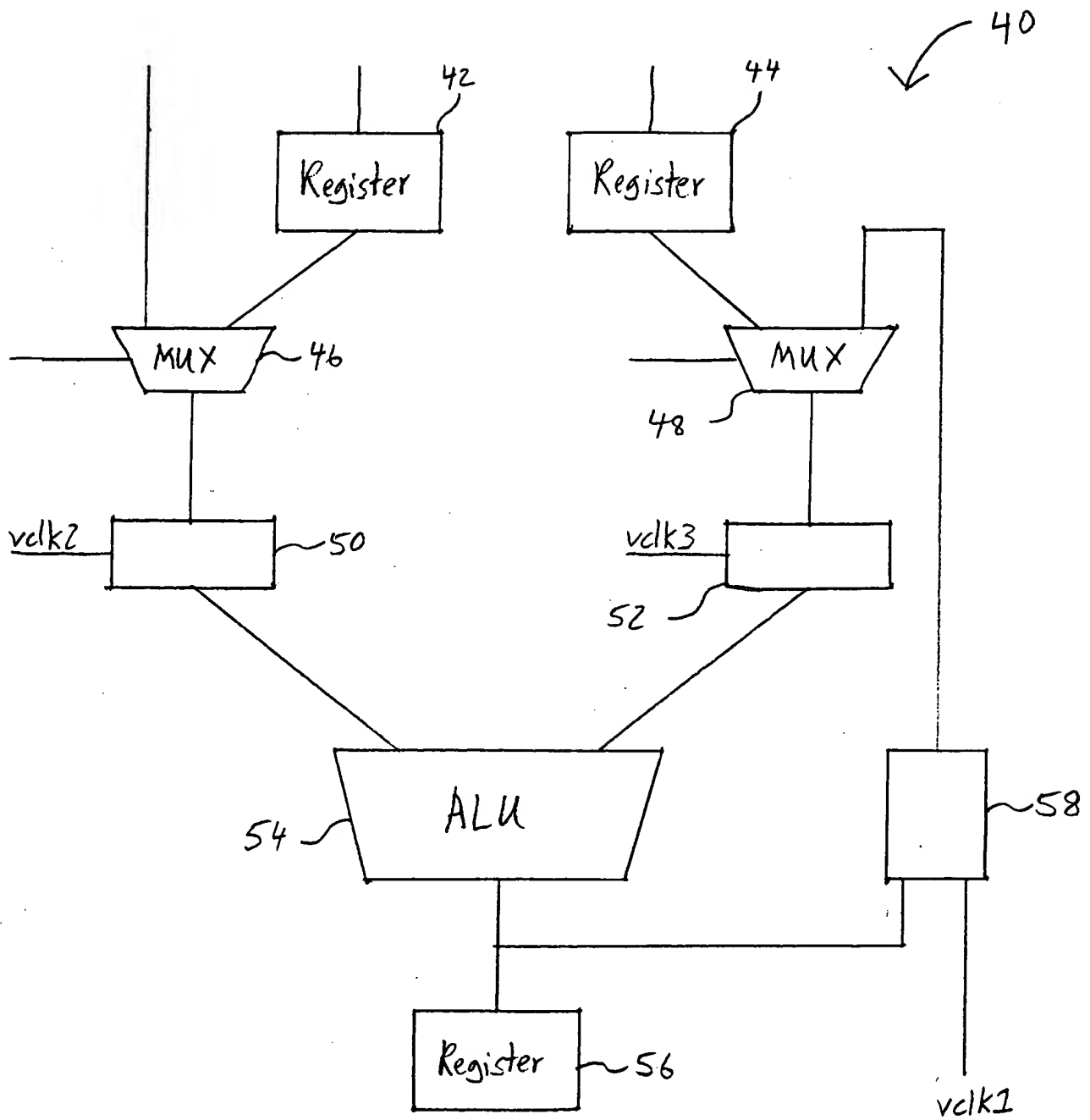


Fig. 5

